;-------------------------------------------------------------------

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; SAB C505C Processor Declarations

;-------------------------------------------------------------------

$SAVE

$NOLIST

;

; BYTE Register

P0 DATA 080H

SP DATA 081H

DPL DATA 082H

DPH DATA 083H

WDTREL DATA 086H

PCON DATA 087H

TCON DATA 088H

PCON1 DATA 088H ; in mapped SFR area

TMOD DATA 089H

TL0 DATA 08AH

TL1 DATA 08BH

TH0 DATA 08CH

TH1 DATA 08DH

P1 DATA 090H

P1ANA DATA 090H

XPAGE DATA 091H

DPSEL DATA 092H

SCON DATA 098H

SBUF DATA 099H

P2 DATA 0A0H

IEN0 DATA 0A8H

IP0 DATA 0A9H

SRELL DATA 0AAH

P3 DATA 0B0H

SYSCON DATA 0B1H

IEN1 DATA 0B8H

IP1 DATA 0B9H

SRELH DATA 0BAH

IRCON DATA 0C0H

CCEN DATA 0C1H

CCL1 DATA 0C2H

CCH1 DATA 0C3H

CCL2 DATA 0C4H

CCH2 DATA 0C5H

CCL3 DATA 0C6H ; in C505CA only

CCH3 DATA 0C7H ; in C505CA only

T2CON DATA 0C8H

CRCL DATA 0CAH

CRCH DATA 0CBH

TL2 DATA 0CCH

TH2 DATA 0CDH

PSW DATA 0D0H

ADCON0 DATA 0D8H

ADDAT DATA 0D9H

ADDATH DATA 0D9H ; C505CA only

ADST DATA 0DAH

ADDATL DATA 0DAH ; C505CA only

ADCON1 DATA 0DCH

ACC DATA 0E0H

P4 DATA 0E8H

B DATA 0F0H

VR0 DATA 0FCH ; in mapped SFR area

VR1 DATA 0FDH ; in mapped SFR area

VR2 DATA 0FEH ; in mapped SFR area

; BIT Register

; PSW

CY BIT 0D7H

AC BIT 0D6H

F0 BIT 0D5H

RS1 BIT 0D4H

RS0 BIT 0D3H

OV BIT 0D2H

F1 BIT 0D1H

P BIT 0D0H

; TCON

TF1 BIT 08FH

TR1 BIT 08EH

TF0 BIT 08DH

TR0 BIT 08CH

IE1 BIT 08BH

IT1 BIT 08AH

IE0 BIT 089H

IT0 BIT 088H

; PCON1 in mapped SFR area

EWPD BIT 08FH

WS BIT 08CH

; IEN0

EA BIT 0AFH

WDT BIT 0AEH

ET2 BIT 0ADH

ES BIT 0ACH

ET1 BIT 0ABH

EX1 BIT 0AAH

ET0 BIT 0A9H

EX0 BIT 0A8H

; IEN1

EXEN2 BIT 0BFH

SWDT BIT 0BEH

EX6 BIT 0BDH

EX5 BIT 0BCH

EX4 BIT 0BBH

EX3 BIT 0BAH

ECAN BIT 0B9H

EADC BIT 0B8H

; P3

RD BIT 0B7H

WR BIT 0B6H

T1 BIT 0B5H

T0 BIT 0B4H

INT1 BIT 0B3H

INT0 BIT 0B2H

TXD BIT 0B1H

RXD BIT 0B0H

; SCON

SM0 BIT 09FH

SM1 BIT 09EH

SM2 BIT 09DH

REN BIT 09CH

TB8 BIT 09BH

RB8 BIT 09AH

TI BIT 099H

RI BIT 098H

; T2CON

T2PS BIT 0CFH

I3FR BIT 0CEH

T2R1 BIT 0CCH

T2R0 BIT 0CBH

T2CM BIT 0CAH

T2I1 BIT 0C9H

T2I0 BIT 0C8H

; ADCON0

BD BIT 0DFH

CLK BIT 0DEH

BSY BIT 0DCH

ADM BIT 0DBH

MX2 BIT 0DAH

MX1 BIT 0D9H

MX0 BIT 0D8H

; IRCON

EXF2 BIT 0C7H

TF2 BIT 0C6H

IEX6 BIT 0C5H

IEX5 BIT 0C4H

IEX4 BIT 0C3H

IEX3 BIT 0C2H

IADC BIT 0C0H

; P1

T2 BIT 097H

CLKOUT BIT 096H

T2EX BIT 095H

INT5 BIT 092H

INT4 BIT 091H

; P1ANA in mapped SFR area

EAN7 BIT 097H

EAN6 BIT 096H

EAN5 BIT 095H

EAN4 BIT 094H

EAN3 BIT 093H

EAN2 BIT 092H

EAN1 BIT 091H

EAN0 BIT 090H

$RESTORE